

Clock Signals

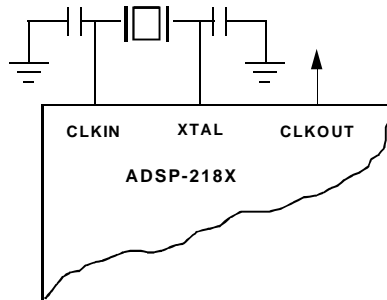


Figure 7-1. External Crystal Connections

Due to the high operating processor core clock speed requirements on some of the ADSP-218x DSPs, it may be advantageous to use a third-overtone crystal rather than a fundamental frequency crystal as an input clock signal in your design. [Figure 7-2](#) shows a sample third overtone schematic.

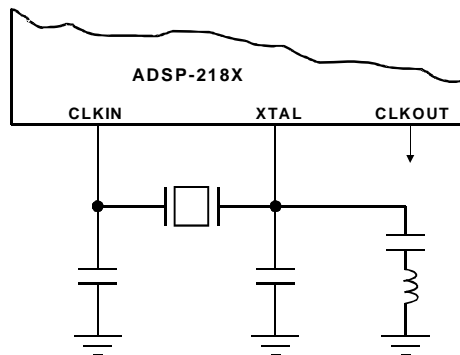


Figure 7-2. Third-Overtone Crystal